

## AMENDMENTS TO THE SPECIFICATION

**Please add the following descriptions of the figures to the section titled “Brief Description of the Drawings,” on page 5 after line 9 beginning with the paragraph “Figure 7 is a functional block diagram...” and prior to the new section heading for “Detailed Description of Preferred Exemplary Embodiments” :**

Figure 8 is a block diagram of a Dy4 CHAMP AV Quad PowerPC Card;

Figure 9 is a block diagram of a Dy4 CHAMP AV II Quad PowerPC Card; and

Figure 10 is a block diagram of an alternative embodiment illustrating a 16-board system configuration.

**The following amended paragraph replaces the third paragraph, page 7, beginning on line 21 and ending on page 8, line 6 in the specification:**

An example application of the present invention is illustrated in Figures 2, 3 and 4 and described below. The three diagrams representing the exemplary embodiment system from different perspectives. In Figure 2, Application Example: Network Topology, a network is constructed from the two fundamental switch components, edge nodes 21 and switches 22. The edge nodes 21 are the termination points for packets crossing the fabric 23. Each PMC 20 of the implementation illustrated uses a switch-fabric-to-PCI bridge 21 that contains two edge nodes EN. The second component illustrated is a fabric switch 22. The processors or DSP's 25 are connected to a PCI

bus. The processors 25 of the circuit cards, are illustrated as a node (Node 1 through Node 7) in Figure 2. The system is comprised of 10 edge nodes EN1 - EN10 and 5 switches S1 - S5, corresponding to the use of five PMC modules. The network is configured in a mesh topology where each switch has a direct connection to each of the other four switches. This a generic topology, which by virtue of it's symmetry is suited to a random data traffic pattern where each node is sending equally to all the other nodes. For this scenario it can be shown that the fabric has much more capacity than needed to manage the throughput available from the two fabric ports on each PMC 20.